



[HP HP0-069](#)

Exam Name: HP Integrity Mid-Range Server Technologies

Q & A : 134 Q&As

Pdf Demo

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Exam : HP HP0-069

Title : HP Integrity Mid-Range Server Technologies

1. What is the function of the cell controller on a cell board in HP Integrity mid-range servers?

- A. provides PCI bus technology for each I/O card cage
- B. enables "chip-kill" like redundancy of cache
- C. replaces the Service and Management Station (SMS) or Test Station
- D. provides connections to crossbar, CPU, memory, and I/O components

Answer: D

2. You are installing a rx8640-32 server with HP-UX 11i v2 in a Fibre Channel SAN solution. The SAN environment consist of HP StorageWorks EVA8000 and MSA1500cs. Which multi-pathing component do you have to configure?

- A. MultiPath I/O (MPIO)
- B. Emulex Multi-pathing driver
- C. LSI Logic Multi-pathing driver
- D. Physical Volume Links (PVLlinks)

Answer: D

3. Which additional hardware component is required to create four nPars on an rx8640 server?

- A. IOX (I/O Expansion Unit)
- B. SEU-2 (System Expansion Unit)
- C. No additional hardware component is required
- D. additional power supply for expanded power consumption

Answer: B

4. Which are features of the rx7640 and rx8640 servers compared to the rx7620 and rx8620 servers? (Select two.)

- A. redundant system backplane
- B. no single point of failure
- C. redundant PCI power supplies
- D. redundant PCI-backplane
- E. redundant clock source

Answer: CE

5. What is the minimum number of cell boards required to use more than six internal disks in an rx8640 with SEU-2 configuration?

- A. 1
- B. 2
- C. 3
- D. 4

Answer: D

6. HP Integrity Virtual Machines can run in which partition types? (Select two.)

- A. vPars
- B. nPars
- C. Standalone Servers
- D. Secure Resource Partitions

Answer: BC

7. What are functions of the MP on Integrity servers? (Select two.)

- A. performs OLRAD
- B. initializes system hardware
- C. reports, logs, and controls system status
- D. controls the installation of firmware updates
- E. deallocates the processor and memory during a failure

Answer: CD

8. What are features of the cell controller? (Select two.)

- A. RAID memory
- B. SCSI controller
- C. Interleaved memory support
- D. Error checking and correcting
- E. Automatic Power Consumption control

Answer: CD

9. Which statement highlights a feature of iCAP and processor cache error resiliency on HP Integrity servers running HP-UX?

- A. There is offline activation of an unused iCAP processor.
- B. There is online activation of an unused iCAP processor.
- C. An iCAP processor can be activated locally by the system administrator.
- D. Remote activation of iCAP processors is no longer necessary on HP Integrity servers running HP-UX 11i.

Answer: B

10. Which tool is used to configure nPars?

- A. HP Partition Manager
- B. nPars Insight Allocator

- C. HP OpenView Partition Wizard
- D. HP Systems Insight Manager (HP SIM)

Answer: A

11. What is the minimum number of Itanium 2 single core 1.6GHz processor modules that can be installed on an Integrity rx7640 server cell board?

- A. 1
- B. 2
- C. 4
- D. 8

Answer: B

12. If an HP partner requires information about porting software, which resource should he use?

- A. HP Product Bulletin
- B. SalesBuilder for Windows (SBW)
- C. Developer and Solution Partner Program (DSPP)
- D. HP-UX 11i v2 System Administrator Reference Guide

Answer: C

13. What enables the new sx2000 chipset to increase the system crossbar bandwidth by a factor of four?

- A. The clock frequency is four times higher.
- B. The sx2000 chipset uses four separate crossbars.
- C. The sx2000 uses the new high-speed SERDES technology.
- D. The crossbar cache uses the new DDR2 SDRAM @ 267/533 MHz.

Answer: C

14. Which resource should be used to find technical specifications for hardware and software?

- A. Product Bulletin
- B. SalesBuilder for Windows (SBW)
- C. PartSurfer
- D. Enterprise Configurator

Answer: A

15. What functionality does Global Shared Memory enable in the sx2000 chipset-based HP Integrity servers?

- A. All partitions read and write from the same memory pool.
- B. All processors within a partition read and write from the same memory pool.
- C. Processors may read from "sharing windows" in other partition's memory.
- D. All nPars can access a limited amount of memory for fault protection.

Answer: C

16. What is the maximum number of internal DVD-ROM drives in an HP Integrity rx7620 server?

- A. 0
- B. 1
- C. 2
- D. 4

Answer: B

17. Which new features differentiate the sx2000 chipset from the sx1000 chipset? (Select three.)

- A. double chip spare
- B. multiple crossbar interfaces per cell
- C. spare channel for crossbar interface
- D. eight memory buses with up to 32 DIMMs total
- E. four front-side buses with up to two sockets each

Answer: ABC

18. What is the benefit of cell-local memory (CLM)?

- A. can be used from other hardware partitions
- B. can be used as virtual memory within a cell board
- C. provides memory protection within a cell board (Chip Memory Sparing)
- D. provides faster memory access to local CPUs

Answer: D

19. Which rule needs to be followed to prevent memory performance bottlenecks in a HP Integrity rx7640/8640 multi-cell partition environment?

- A. load memory symmetrically across all cells within a partition
- B. firmware is taking care and no loading order needs to be followed
- C. load memory symmetrically on the first cell for optimal performance
- D. operating system takes care and will use the memory with lowest latency

Answer: A

20. Which ASICs are used in the sx1000 chipset? (Select two.)

- A. Peripheral bay
- B. Memory Adapter
- C. Cell Controller (CC)
- D. Core I/O Controller (CIC)
- E. System Bus Adapter (SBA)

Answer: CE

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