



[HP HP0-P16](#)

Exam Name: *Planning&Design of HP 9000/HP Integrity Server Solutions*

Q & A : 145 Q&As

Pdf Demo

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Title : Planning&Design of HP 9000/HP Integrity Server Solutions

1. Which product minimizes the need for a system administrator to be physically at the console performing diagnostics and management tasks?

- A. System Management Homepage
- B. Webmin Admin
- C. Management Processor
- D. Systems Insight Manager

Answer: C

2. Which statement is true for directory-based cache coherence on cell-based systems?

- A. It is used to ensure consistency between main memory and I/O caches.
- B. It is used to ensure consistency between processor caches, I/O caches, and main memory.
- C. It is used to ensure consistency between processor caches and main memory only.
- D. It is used to ensure consistency between processor caches only.

Answer: B

3. Which statements are correct for the PA-8900 processor family? (Select two.)

- A. PA-8900 is a dual-die processor.
- B. PA-8900 supports a runway bus.
- C. PA-8900 supports a frontside bus.
- D. PA-8900 is a single-core processor.
- E. PA-8900 is a dual-core per die processor.

Answer: CE

4. The sx2000 chipset is used in which HP Integrity servers? (Select two.)

- A. HP Integrity rx8640
- B. HP Integrity rx6600
- C. HP Integrity rx3600
- D. HP Integrity Superdome
- E. HP Integrity rx7620

Answer: AD

5. Your customer wants to protect his systems against data loss. Which HP service would you recommend?

- A. Adaptive Enterprise Services
- B. Application Management Services
- C. Continuity and Availability Services
- D. Infrastructure Management Services

Answer: C

6. What is a feature of vPar virtualization technology for HP Integrity servers?

- A. It provides hardware fault isolation.
- B. It enables multiple copies of HP-UX to run on the server.
- C. It allows only one operating system image per HP Integrity server.
- D. It provides dedicated processor and memory, and shared PCI cards.

Answer: B

7. What are the advantages of error checking and correcting (ECC) compared to parity protection? (Select two.)

- A. multi-bit error detection
- B. multi-bit error detection and correction
- C. single-bit error detection and correction
- D. faster single/multi-bit error detection and correction

Answer: AC

8. By what factor is the memory bandwidth of the zx2 chipset increased compared to the zx1 chipset?

- A. 1.0
- B. 1.2
- C. 2.0
- D. 2.5

Answer: C

9. Which HP service provides porting and migration capabilities?

- A. HP Managed Services
- B. HP Infrastructure Services
- C. HP IT Management Solutions
- D. HP Itanium Lifecycle Services

Answer: D

10. The sx1000 chipset is used in which HP Integrity server?

- A. HP Integrity rx8640
- B. HP Integrity rx6600
- C. HP Integrity rx3600

D. HP Integrity rx7620

Answer: D

11. What I/O bus type is not supported on the zx2 chipset, as compared to the zx1 chipset?

A. PCI-X

B. PCI

C. PCI-Express

D. AGP 8x

Answer: D

12. Which new features are available on the zx2 chipset, as compared to the zx1 chipset? (Select two.)

A. SDRAM

B. chip spare

C. double chip spare

D. up to 512GB main memory

E. up to 768GB main memory

Answer: CE

13. Which hardware components on HP Integrity rx8640 and rx7640 servers are non-redundant? (Select three.)

A. PCI I/O fans

B. PCI cardcage

C. system backplane

D. front and rear fans

E. PCI power supplies

F. bulk power supplies

Answer: BCE

14. The zx2 chipset is used in which HP Integrity servers? (Select two.)

A. HP Integrity rx7640

B. HP Integrity rx2660

C. HP Integrity Superdome

D. HP Integrity rx3600

E. HP Integrity rx4640

Answer: BD

15. Which new features differentiate the sx2000 chipset from the sx1000 chipset? (Select three.)

A. double chip spare

B. multiple crossbar interfaces per cell

C. spare channel for crossbar interface

D. eight memory buses with up to 32 DIMMs total

E. four frontside buses with up to two sockets each

Answer: ABC

16. By what factor is the I/O bandwidth of the zx2 chipset increased compared to the zx1 chipset?

A. 1.0

B. 1.2

C. 2.0

D. 2.5

Answer: C

17. When Hyper-Threading is enabled, what is the operating system allowed to do?

A. ensure that applications run faster

B. create virtual machines

C. allocate more memory to an application

D. schedule two threads or processes simultaneously

Answer: D

18. What differentiates the HP Integrity platform from the IBM pSeries?

A. HP Integrity does not run Linux.

B. IBM pSeries does not run Linux.

C. HP Integrity does not run Windows.

D. IBM pSeries does not run Windows.

Answer: D

19. What new feature is enabled by the zx2 chipset, as compared to the zx1 chipset?

A. PCI-X 1.0

B. SDRAM

C. chip spare

D. PCI Express

Answer: D

20. Which statements are true for an Intel Itanium 2 processor? (Select two.)

A. It supports a frontside bus.

B. It is a RISC processor.

C. It is an EPIC architecture-based processor.

D. It uses internal 32-bit processors with a 64-bit external bus interface.

E. It uses internal 64-bit processors with a 32-bit external bus interface.

Answer: AC

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